PATENT Docket No.: 60188-540

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of

ruhito OHNISHI, et al.

Customer Number: 20277

Confirmation Number: 4861

Serial No.: 10/603,737

Group Art Unit: 2811

Filed: June 26, 2003

Examiner: not yet assigned

BIPOLAR TRANSISTOR DEVICE HAVING PHOSPHOROUS For:

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office Sir: in the above-referenced application. It is noted that the first Applicant and the Title is incorrect.

Attached is a copy of the Declaration and Power of Attorney and Preliminary Amendment filed on June 26,2003, which evidences that the First Applicant should appear as:

TERUHITO OHNISHI, OSAKA, JAPAN;

And the Title should appear as:

BIPOLAR TRANSISTER DEVICE HAVING PHOSPHOROUS

It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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WDC99 821345-1.060188.0540



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CONFIRMATION NO. 4861

McDERMOTT, WILL & EMERY 600 13th Street, N.W. Washington, DC 20005-3096

FILING RECEIPT *OC000000010904548*

Date Mailed: 09/22/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Teruhit Ohnishi, Osaka, JAPAN; Akira Asai, Osaka, JAPAN; -

Assignment For Published Patent Application

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., Kadoma-shi, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 10/009,201 12/10/2001 which is a 371 of PCT/JP01/03979 05/14/2001

Foreign Applications

JAPAN 2000-140292 05/12/2000

If Required, Foreign Filing License Granted: 09/22/2003

Projected Publication Date: 01/01/2004

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Non-Publication Request: No

Early Publication Request: No

McDERMOTT, WILL & EMERY

Title

Semiconductor device and method for fabricating the same

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